

Pin name	Linux	SOPine Pin	Direction	Default Function	Default Pull	Function 2	Function 3	Function 4	Function 5	Function 6	SOPine Name
PB0	GPIO32	35	I/O	DIS	Z	UART2_TX		JTAG_MS0		PB_EINT0	PB0-UART2_TX
PB1	GPIO33	75	I/O	DIS	Z	UART2_RX		JTAG_CK0	SIM_PWREN	PB_EINT1	PB1-UART2_RX
PB2	GPIO34	36	I/O	DIS	Z	UART2_RTS		JTAG_DO0	SIM_VPPEN	PB_EINT2	PB2
PB3	GPIO35	30	I/O	DIS	Z	UART2_CTS	I2S0_MCLK	JTAG_DI0	SIM_VPPPP	PB_EINT3	PB3-I2S_MCLK
PB4	GPIO36	28	I/O	DIS	Z	AIF2_SYNC	PCM0_SYNC		SIM_CLK	PB_EINT4	PB4-I2S_SYNC
PB5	GPIO37	27	I/O	DIS	Z	AIF2_BCLK	PCM0_BCLK		SIM_DATA	PB_EINT5	PB5-I2S_BCLK
PB6	GPIO38	39	I/O	DIS	Z	AIF2_DOUT	PCM0_DOUT		SIM_RST	PB_EINT6	PB6-I2S_DOUT
PB7	GPIO39	29	I/O	DIS	Z	AIF2_DIN	PCM0_DIN		SIM_DET	PB_EINT7	PB7-I2S_DIN
PB8	GPIO40	34	I/O	DIS	Z			UART0_TX		PB_EINT8	PB8
PB9	GPIO41	33	I/O	DIS	Z			UART0_RX		PB_EINT9	PB9
PC0	GPIO64	134	I/O	DIS	Z	NAND_WE		SPI0_MOSI			PC0-SPI0_MOSI
PC1	GPIO65	150	I/O	DIS	Z	NAND_ALE	SDC2_DS	SPI0_MISO			PC1-SPI0_MISO
PC2	GPIO66	142	I/O	DIS	Z	NAND_CLE		SPI0_CLK			PC2-SPI0_CLK
PC3	GPIO67	148	I/O	DIS	Pull-Up	NAND_CE1		SPI0_CS			PC3-SPI0_CS
PC4	GPIO68	135	I/O	DIS	Pull-Up	NAND_CE0					PC4
PC5	GPIO69	25	I/O	DIS	Z	NAND_RE	SDC2_CLK				PC5
PC6	GPIO70	154	I/O	DIS	Pull-Up	NAND_RB0	SDC2_CMD				PC6
PC7	GPIO71	132	I/O	DIS	Pull-Up	NAND_RB1					PC7
PC8	GPIO72	130	I/O	DIS	Z	NAND_DQ0	SDC2_D0				PC8
PC9	GPIO73	153	I/O	DIS	Z	NAND_DQ1	SDC2_D1				PC9
PC10	GPIO74	156	I/O	DIS	Z	NAND_DQ2	SDC2_D2				PC10
PC11	GPIO75	165	I/O	DIS	Z	NAND_DQ3	SDC2_D3				PC11
PC12	GPIO76	146	I/O	DIS	Z	NAND_DQ4	SDC2_D4				PC12
PC13	GPIO77	157	I/O	DIS	Z	NAND_DQ5	SDC2_D5				PC13
PC14	GPIO78	155	I/O	DIS	Z	NAND_DQ6	SDC2_D6				PC14
PC15	GPIO79	133	I/O	DIS	Z	NAND_DQ7	SDC2_D7				PC15
PC16	GPIO80	144	I/O	DIS	Z	NAND_DQS	SDC2_RST				PC16
PD0	GPIO96	60	I/O	DIS	Z	LCD_D2	UART3_TX	SPI1_CS	CCIR_CLK		PD0-SPI1_CS
PD1	GPIO97	49	I/O	DIS	Z	LCD_D3	UART3_RX	SPI1_CLK	CCIR_DE		PD1-SPI1_CLK
PD2	GPIO98	57	I/O	DIS	Z	LCD_D4	UART4_TX	SPI1_MOSI	CCIR_HSYNC		PD2-SPI1_MOSI
PD3	GPIO99	61	I/O	DIS	Z	LCD_D5	UART4_RX	SPI1_MISO	CCIR_VSYNC		PD3-SPI1_MISO
PD4	GPIO100	52	I/O	DIS	Z	LCD_D6	UART4_RTS		CCIR_D0		PD4-UART4_RTS
PD5	GPIO101	44	I/O	DIS	Z	LCD_D7	UART4_CTS		CCIR_D1		PD5-UART4_CTS
PD6	GPIO102	45	I/O	DIS	Z	LCD_D10			CCIR_D2		PD6
PD7	GPIO103	40	I/O	DIS	Z	LCD_D11			CCIR_D3		PD7
PD8	GPIO104	73	I/O	DIS	Z	LCD_D12		RGMII_RXD3/RMII_NULL	CCIR_D4		GRXD3
PD9	GPIO105	83	I/O	DIS	Z	LCD_D13		RGMII_RXD2/RMII_NULL	CCIR_D5		GRXD2
PD10	GPIO106	51	I/O	DIS	Z	LCD_D14		RGMII_RXD1/RMII_RXD1			RMII-RXD1
PD11	GPIO107	48	I/O	DIS	Z	LCD_D15		RGMII_RXD0/RMII_RXD0			RMII-RXD0
PD12	GPIO108	91	I/O	DIS	Z	LCD_D18	LVDS_VP0	RGMII_RXCK/RMII_NULL			GRXCK
PD13	GPIO109	89	I/O	DIS	Z	LCD_D19	LVDS_VN0	RGMII_RXCT/RMII_CRS_DV			RMII-CRS-DV
PD14	GPIO110	87	I/O	DIS	Z	LCD_D20	LVDS_VP1	RGMII_NULL/RMII_RXER			RMII-RXER
PD15	GPIO111	80	I/O	DIS	Z	LCD_D21	LVDS_VN1	RGMII_TXD3/RMII_NULL	CCIR_D6		GTXD3
PD16	GPIO112	82	I/O	DIS	Z	LCD_D22	LVDS_VP2	RGMII_TXD2/RMII_NULL	CCIR_D7		GTXD2
PD17	GPIO113	78	I/O	DIS	Z	LCD_D23	LVDS_VN2	RGMII_TXD1/RMII_TXD1			RMII-TXD1
PD18	GPIO114	85	I/O	DIS	Z	LCD_CLK	LVDS_VPC	RGMII_TXD0/RMII_TXD0			RMII-TXD0
PD19	GPIO115	79	I/O	DIS	Z	LCD_DE	LVDS_VNC	RGMII_TXCK/RMII_TXCK			RMII-TXCK
PD20	GPIO116	47	I/O	DIS	Z	LCD_HSYNC	LVDS_VP3	RGMII_TXCTL/RMII_TXEN			RMII-TXEN
PD21	GPIO117	74	I/O	DIS	Z	LCD_VSYNC	LVDS_VN3	RGMII_CLKINRMII_NULL			GCLKIN



<b>PL6</b>	GPIO358	16	I/O	DIS	Z	S_JTAG_DO			S_PL_EINT6	AP-WAKE-BT
<b>PL7</b>	GPIO359	24	I/O	DIS	Z	S_JTAG_DI			S_PL_EINT7	PL7
<b>PL8</b>	GPIO360	15	I/O	DIS	Z	S_I2C_CLK			S_PL_EINT8	PL8-S_TWI_SCK
<b>PL9</b>	GPIO361	17	I/O	DIS	Z	S_I2C_SDA			S_PL_EINT9	PL9-S_TWI_SDA
<b>PL10</b>	GPIO362	166	I/O	DIS	Z	S_PWM			S_PL_EINT10	PL10-S_PWM
<b>PL11</b>	GPIO363	13	I/O	DIS	Z	S_CIR_RX			S_PL_EINT11	PL11-IR_RX

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